10th Workshop on Parallel Systems and Algorithms
PASA 2012
in conjunction with
Munich, Germany, February 29, 2012

Workshop Program

08:30 Welcome, J. Keller, R. Wanka

08:30 – 09:30 Session 1: Codes and Networks (Session Chair: TBD)
Resilient data encoding for fault-prone signal transmission in parallelized signed-digit based arithmetic, D. Neuhäuser, E. Zehendner
The Spectral Relation between the Cube-Connected Cycles and the Shuffle-Exchange Network C. Riess, V. Strehl, R. Wanka

09:30 – 10:30 Session 2: Applications (Session Chair: TBD)
Parallelization Strategies to Speed-Up Computations for Terrain Analysis on Multi-Core Processors, S. Schiele, H. Błaar, D. Thürkow, M. Möller, M. Müller-Hanneman
A Speed-Up Study for a Parallelized White Light Interferometry Preprocessing Algorithm on a Virtual Embedded Multiprocessor System, D. Schoenwetter, M. Schneider, D. Fey

10:30 – 11:00 Break

11:00 – 12:00 Session 3: Infrastructure and Storage Systems (Session Chair: TBD)
Achieving scalability for job centric monitoring in a distributed infrastructure, M. Hilbrich, R. Müller-Pfefferkorn
Parallel coding for storage systems – An OpenMP and OpenCL capable framework, P. Sobe

12:00 – 12:30 Meeting of FG PARS (Mitgliederversammlung und Leitungsgremium)

12:30 – 13:45 Lunch Break

13:45 – 14:00 PARS Prize

14:00 – 15:15 Keynote (Session Chair: TBD)
Fooling the masses with performance results: Classic bestsellers and some new ideas G. Wellein

15:15 – 15:45 Break

15:45 – 16:45 Session 4: Scheduling and I/O (Session Chair: TBD)
I/O-efficient approximation of graph diameters by parallel cluster growing – a first experimental study, D. Ajwani, A. Beckmann, U. Meyer, D. Veith
Flexible Scheduling and Thread Allocation for Synchronous Parallel Tasks C. W. Kessler, E. Hansson

16:45 Closing Remarks, J. Keller, R. Wanka

Note: at 17:00, there is a keynote talk by K. De Bosschere at the ARCS conference